

Appl. No. 10/707,514
Amdt. dated April 06, 2005
Reply to Office action of January 13, 2005

AMENDMENTS TO THE SPECIFICATION

In paragraph [0034]:

- 5 In order to shed light on an embodiment according to the present invention, the following description first discusses an embodiment of a particular frequency division and then expands on the general applications. Please refer to Fig. 6. Fig. 6 is a diagram of a frequency
- 10 division circuit 30 located in a signal circuit 32 for achieving M/4 frequency division (M is an integer). To achieve frequency division, besides the frequency division circuit 30, an oscillator is located in the signal circuit 32 for providing four clocks CK_1 to CK_4
- 15 as reference clocks. The period of each of the four clocks CK_1 to CK_4 is T, and the phases of the four clocks are uniformly distributed in 360 degrees. In other words, the phase difference between CK_n (n=1 to 4) and CK_1 is equivalent to the time difference of $(n-1)*T/4$. A
- 20 general form of this equation is $((n-1)/N)*360$ degrees, which expresses the phase difference between an n-th clock and the first clock when there are N clocks.